

4855956



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SERIAL
NUMBER
(Series
of 1987)

103312

PATENT DATE

AUG 08 1989

PATENT
NUMBER

SERIAL NUMBER

07/103,312

FILING DATE

10/01/87

CLASS

365

SUBCLASS

DIE

GROUP PART UNIT

233

EXAMINER

Peprik

APPLICANTS

TAKAHIKO HIRAI, TOKYO, JAPAN.

CONTINUING DATA***
VERIFIED

NONE/8-

FOREIGN/PCT APPLICATIONS***
VERIFIED

JAPAN

61-234929

10/01/86

Checkoff

JPX

Foreign priority claimed
35 USC 119 conditions met☒ yes
☐ no☐ yes
☐ noAS
FILEDSTATE OR
COUNTRYSHEETS
DRWGS.TOTAL
CLAIMSINDEP.
CLAIMSFILING FEE
RECEIVEDATTORNEY'S
DOCKET NO.

Verified and Acknowledged

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FILEDSTATE OR
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DRWGS.TOTAL
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DOCKET NO.

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TITLE

SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CELL ARRANGEMENT

U.S. DEPT. of COMM. Pat. & Trademark Office (rev. 10-78)

CERTIFICATE

FEB 12 1991

OF CORRECTION

PARTS OF APPLICATION
FILED SEPARATELY

3 - sub. pths. of Drawg. Sub. 9, 1989

NOTICE OF ALLOWANCE MAILED

PREPARED FOR ISSUE 3-27-89

CLAIMS ALLOWED

MAR 24 1989

Assistant Examiner.

Docket Clerk

Total Claims

6

Print Claim

1

ISSUE FEE

Amount Due

620.00

Date Paid

6/12/89

JOSEPH A. POPEK
PRIMARY EXAMINER
ART UNIT 233

Primary Examiner

DRAWING

Sheets Drwg.

3

Figs. Drwg.

6

Print Fig.

3

ISSUE CLASSIFICATION

Class

365

Subclass

210

ISSUE
BATCH
NUMBER

R38

Label
Area

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